

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1969	core adj processor	US-PGPUB; USPAT	OR	ON	2006/01/19 11:18
L2	435	microengine	US-PGPUB; USPAT	OR	ON	2006/01/19 11:19
L3	84	1 same 2	US-PGPUB; USPAT	OR	ON	2006/01/19 11:19
L4	3330	csr	US-PGPUB; USPAT	OR	ON	2006/01/19 11:19
L5	569	configuration adj2 status adj register	US-PGPUB; USPAT	OR	ON	2006/01/19 11:19
L6	3835	4 or 5	US-PGPUB; USPAT	OR	ON	2006/01/19 11:20
L7	25	3 and 6	US-PGPUB; USPAT	OR	ON	2006/01/19 13:56
L8	21	lc-trie	US-PGPUB; USPAT	OR	ON	2006/01/19 13:56
L9	23	lc adj trie	US-PGPUB; USPAT	OR	ON	2006/01/19 13:57
L10	23	8 or 9	US-PGPUB; USPAT	OR	ON	2006/01/19 14:16
L11	1	6 and 10	US-PGPUB; USPAT	OR	ON	2006/01/19 13:57
L12	5	multiple adj element adj comput\$	US-PGPUB; USPAT	OR	ON	2006/01/19 14:17
L13	111	1.ab.	US-PGPUB; USPAT	OR	ON	2006/01/19 14:17
L14	47	2.ab.	US-PGPUB; USPAT	OR	ON	2006/01/19 14:17
L15	0	13 and 14	US-PGPUB; USPAT	OR	ON	2006/01/19 14:18
L16	65438	processor.ab.	US-PGPUB; USPAT	OR	ON	2006/01/19 14:18
L17	31	14 and 16	US-PGPUB; USPAT	OR	ON	2006/01/19 14:20
L18	22	17 not 7	US-PGPUB; USPAT	OR	ON	2006/01/19 14:20